

Alston & Bird LLP

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July 10, 2008

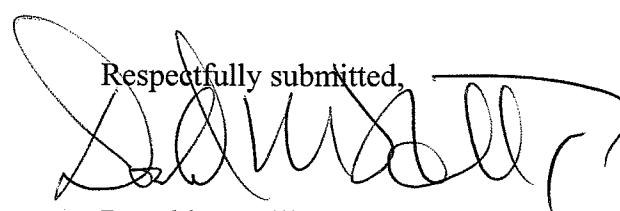
Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: *United States Patent for Method Of And Apparatus For Testing For Integrated Circuit Contact Defects*
Application No. 10/533,188; Filed September 28, 2005
Patent No. 7,385,410; Issued June 10, 2008
Our File 040857/291463

Sir:

It is respectfully requested that a Certificate of Correction be issued for the above-identified patent, in accordance with 37 C.F.R. §1.322. This request is made in order to correct the mistakes incurred through the fault of the United States Patent and Trademark Office ("USPTO").

The mistakes appearing in the patent are set forth on the Certificate of Correction e-filed herewith.

Respectfully submitted,

Donald M. Hill, Jr.
Registration No. 40,646

DMH:mf
Enclosure

LEGAL02/30873255v1

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,385,410 B2

Page 1 of 1

APPLICATION NO. : 10/533,188

ISSUE DATE : June 10, 2008

INVENTOR(S) : Payman

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14.

Line 37, "pinsfonning" should read --pins forming--;

Line 60, "hi" should read --M--.

Column 15.

Line 14, "non-liner" should read --non-linear--.

Column 16.

Line 1, "cotuiection" should read --connection--;

Line 15, "ar" should read --or--;

Line 26, "die" should read --the--.

MAILING ADDRESS OF SENDER

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